

AMENDMENTS TO THE CLAIMS

1. (Previously presented) A process for synchronizing an input signal including the following process steps:

demodulating the input signal according to a first demodulation method in relation to a first signal parameter for creating a first demodulated input signal;

correlating the first demodulated input signal with a first comparison signal that depends upon the first demodulation method to determine a time offset between the first demodulated input signal and the first comparison signal; and

time-wise shifting the input signal in accordance with the time-wise offset determined by the correlation;

demodulating the input signal according to a second demodulation method in relation to a second signal parameter for creating a second demodulated input signal; and

correlating the second demodulated input signal with a second comparison signal that depends upon the second demodulation method for determining a time offset between the second demodulated input signal and the second comparison signal.

2. (cancelled)

3. (Previously presented) The process according to claim 1, wherein the first demodulation method is amplitude demodulation and the first signal parameter is the amplitude and the second demodulation method is frequency demodulation in the second signal parameter is frequency.

4. (Previously presented) A process for synchronizing an input signal including the following process steps:

demodulating the input signal according to a first demodulation method in relation to a first signal parameter for creating a first demodulated input signal;

correlating the first demodulated input signal with a first comparison signal that depends upon the first demodulation method to determine a time offset between the first demodulated input signal and the first comparison signal; and

time-wise shifting the input signal in accordance with the time-wise offset determined by the correlation,

wherein the input signal is demodulated according to n different demodulating methods in relation to n different parameters to create n different demodulated input signals, and

wherein each demodulated input signal is correlated with an associated comparison signal dependent on the associated demodulation method to determine a time offset between each demodulated input signal and the associated comparison signal.

5. (previously presented) The process according to claim 4, wherein each demodulation method is defined by subjecting the input signal to one of a definite analytical and partially defined function f in order to create the associated demodulated input signal.

6. (previously presented) The process according to claim 5, wherein at least one of the functions is one of: amplitude demodulation; the logarithm of the amplitude demodulation; frequency demodulation; and the time differential of the frequency demodulation.

7. (Previously presented) The process according to claim 1, wherein the different results of the correlations of the different demodulation methods are subjected to a weighting, with the correlation results of each demodulation method having a predetermined weighting factor applied thereto, for calculating the offset of the input signal.

8. (Canceled)

9. (Currently Amended) A process for synchronizing an input signal including the following process steps:

demodulating the input signal according to a first demodulation method in relation to a first signal parameter for creating a first demodulated input signal;

correlating the first demodulated input signal with a first comparison signal that depends upon the first demodulation method to determine a time offset between the first demodulated input signal and the first comparison signal; and

time-wise shifting the input signal in accordance with the time-wise offset determined by the correlation,

wherein the input signal is subjected to an analog/digital conversion at one of before and after demodulation, and

wherein each result of the correlation is subjected to a first interpolation between sampling points.

10. (Previously Presented) The process according to claim 9, wherein a filtering takes place at one of before and after the analog/digital conversion.

11. (Currently Amended) The process according to ~~claim 10~~claim 12, wherein the filtering is different for each demodulation method.

12. (Currently Amended) The process according to claim 10, wherein the filtering is carried out so that the ~~one first~~ demodulation method is converted into another demodulation method.

13. (canceled)

14. (Currently Amended) The process according to ~~claim 13~~claim 9, wherein a first interpolation method used in the first interpolation depends upon one of: an associated demodulation method, the first comparison signal, f , f_i , and or the filtering.

15. (Currently Amended) The process according to ~~claim 13~~claim 9, wherein the time offset of the input signal is subjected to a second interpolation between sampling points.